

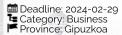
HW ENGINEER FOR THE DESIGN AND TEST OF FIRMWARE ON LOW-POWER MICROCONTROLLERS

Company

Description

https://www.ceit.es/en-GB/about-ceit/centre-profile

Information



Company

Ceit



Main functions, requisites & benefits

Main functions

We are looking for an Electronics Engineer to design efficiently the firmware and software on ad-hoc solutions based on microcontrollers and/or microprocessors of different manufacturers. The objective is to design smart monitoring devices to operate efficiently in harsh environments such as offshore platforms. The knowledge of microcontrollers architectures, internal and external peripherals (timers, ADCs, sensors, memories, communications modules) and interfaces (UART, SPI, I2C) is key for this position. Breakthrough technologies will be used with the aim of digitalizing challenging applications. These systems normally integrate wireless communications modules as another integrated circuit connected to the microcontroller. The knowledge of Linux (design of drivers) and of RTOS will be appreciated. To mount robust prototypes and follow some procedures for electrical and functional testing will be part of the job.

Requisites

Degree: Electronics Engineering / Telecommunications Engineering with Master's Degree. Language: English. Software: C/C++, Eclipse, Linux, RTOS, ARM microcontrollers, GitHub.

Benefits

Incorporation in a company at the forefront of technological knowledge, and with a clear vocation of service to society. Opportunities for growth and professional development, and a good work environment based on trust and teamwork. Remuneration: Remuneration will be commensurate with experience. Start date: Immediate. Schedule: Winter: 7.75 hours per day. July and August: 6 hours a day (continuous). Flexible hours: check-in between 8:00 and 9:30, check-out from 4:15 p.m. Possibility of continuous day on Friday. Teleworking: Option to telework 1 day per week. Holidays: 23 days + Christmas holiday (Dec 24-Jan 2).