




Company Description

CIDETEC is a Technology Centre that brings together three leading international centres in the fields of Energy Storage, Surface Engineering and Nanomedicine. For more than 25 years, we have been working with leading companies to develop technology that makes the world a better place.

Information

 Deadline: 2025-03-18
 Category: Business
 Province: Gipuzkoa

 Country: Spain
 City: San Sebastián

Company

CIDETEC



Main functions, requisites & benefits

Main functions

You will form part of a state-of-the-art research centre geared to sustainability and the circular economy, which has helped electrify Spain's roads and is now working towards 100% future sustainability. You will work on training and/or company transfer projects on the storage systems unit strategic line involving HW and validation and its industrialisation. You must be capable of carrying out several lines of research simultaneously, optimising resources and maximising results. Your work will be geared to: BMS market research, according to the application (automotive, stationary, light mobility, domestic storage, aerospace). Schematic and PCB design to comply with the specifications according to the design standards defined. Provide support for design validation and analysis of results. Provide support for integrating design in the final application. Working on the development of new algorithms for the BMS (Battery Management System) control SW. You will also be involved in: Generating and implementing new ideas/concepts. Drawing up technical documentation. Drawing up technical documentation connected with seeking funding.

Requisites

Education: Engineering degree specialising in Electronics/Telecommunications. Master's or Doctoral Degree in the smart energy systems area. Additional training in Altium and PCB design will be considered a plus. Languages: Fluent in spoken and written English. Knowledge: The successful candidate must demonstrate sound knowledge of the following: Design of schematic diagrams in Altium to implement the functions identified. Experience in PCB design in accordance with IPC standards, emissions and EMC field immunity. Experience in flexible PCB designs. I2C, SPI, CAN, Ethernet communication protocols. Knowledge of applications enabling validation oversight: Matlab, CANalyzer, etc Experience: Over 3 years' experience in an Altium environment or similar for electronic card creation. Experience in card design as per the identified standards, mainly IPC 2221 or similar. Experience in generating documentation in accordance with the ISO 26262 standard. Experience in programming for large projects. Experience in low-level communication protocols such as SPI or I2C. Experience in communication protocols such as CAN or Ethernet. Observations: We are seeking a highly motivated person with an interest in research and innovation, to join a multi-disciplinary team. They must be able to organise their own work and meet deadlines and targets. Having undertaken a placement abroad and more than 3 years' work experience will be considered a plus. *We positively value applications from people with a disability equal to or greater than 33%, in compliance with current legislation, the General Law on the Rights of Persons with Disabilities and their social inclusion (LGD).

Benefits

At CIDETEC you will be able to develop your career alongside a team of top-level professionals dedicated to innovation and seeking to provide practical solutions for a more sustainable world. Professional development opportunities that will help you build a solid career, working on projects to transform industry and society. Work with top-level teams in local, national and European settings. Work-life balance initiatives. Continuous training. A privileged setting in a safe, friendly environment. Variable remuneration package